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Sheet 1 of 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Atty. Docket No.: 303.324US2	Serial No. 09/256,643
	Applicant: Leonard Forbes et al.	
	Filing Date: February 23, 1999	Group: 2822

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
						Yes	No
mt	6,166,401	12/26/2000	Forbes, L.	257	77		08/20/98

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
mt							
	Wolf, "Silicon Processing for the VLSI Era Vol. 2: Process Integration", Lattice Press, 623-626, (1990)						

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Examiner <u>Michael Trice</u>	Date Considered <u>3/04/02</u>
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*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.